



COPY OF PAPERS  
ORIGINALLY FILED

PATENT

09/382,442

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Alan R. Reinberg  
Serial No.: 09/382,442  
Filed: August 25, 1999  
Title: METHOD FOR REDUCING SINGLE BIT DATA LOSS IN A MEMORY CIRCUIT

Examiner: Richard A. Booth  
Group Art Unit: 2812  
Docket: 303.522US1

#15/EXT  
3/14/02  
V. Vannaep

**PETITION FOR A ONE-MONTH EXTENSION OF TIME**

Box RCE  
Commissioner for Patents  
Washington, D.C. 20231

RECEIVED  
MAR 11 2002  
TC 2800 MAIL ROOM

In accordance with the provisions of 37 C.F.R. § 1.136(a), it is respectfully requested that a one-month extension of time be granted in which to respond to the Final Office Action mailed October 11, 2001, said period of response being extended from January 11, 2002 to February 11, 2002.

Please charge Deposit Account No. 19-0743 in the amount of \$110.00 to cover the required extension fee.

Respectfully submitted,

ALAN R. REINBERG

By his Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
P.O. Box 2938  
Minneapolis, MN 55402  
(612) 373-6976

Date 11 February 02

By [Signature]

Janal M. Kalis  
Reg. No. 37,650

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Attn: Box RCE, Commissioner for Patents, Washington, D.C. 20231, on this 11 day of February, 2002.

Name Tina Rysh

Signature [Signature]

03/04/2002 AMNDADF1 00000046 190743 09382442

02 FC:115 110.00 CH